

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	Art Unit:	2195
Hong Jiang et al.	§		
Serial No.:	§	Examiner:	Eric Charles Wai
10/750,583	§		
Filed:	§	Conf. No.:	8582
December 31, 2003	§		
For:	§	Docket:	ITL.1704US
Processing Architecture Having	§	P17510	
Passive Threads and Active	§		
Semaphores	§	Assignee:	Intel Corporation

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

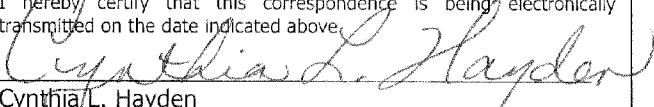
SUPPLEMENTAL REPLY TO FINAL REJECTION

Sir:

In response to the Advisory Action mailed July 12, 2010, please amend the above-referenced patent application as follows:

Date of Deposit: July 29, 2010

I hereby certify that this correspondence is being electronically transmitted on the date indicated above.


Cynthia L. Hayden